

In the Claims:

1. (original) A method for configuring a microcontroller comprising:
  - a) accessing a description of hardware resources of said microcontroller;
  - b) selecting available configurations of said hardware resources of said microcontroller, wherein said selecting produces a selected configuration; and
  - c) generating configuration information corresponding to said selected configuration.
2. (original) The method according to Claim 1 wherein said description of the hardware resources of said microcontroller comprises a text readable data structure.
3. (original) The method according to Claim 2 wherein said text readable data structure is substantially compliant with extensible markup language.
4. (original) The method according to Claim 1 further comprising:
  - a1) accessing predetermined configurations of said hardware resources, wherein said predetermined configurations are user modules.
5. (original) The method according to Claim 4 further comprising:
  - b1) selecting said user modules to produce said selected configuration.
6. (original) The method according to Claim 1 further comprising:
  - c1) generating microprocessor instructions for using said configuration information to configure said microcontroller.
7. (original) The method according to Claim 1 further comprising:
  - c2) generating application programming interface calls for embedded software.

8. (original) The method according to Claim 7 wherein said application programming interface calls are named according to names given to configurations of said hardware resources.
9. (original) The method according to Claim 1 further comprising:
- c3) generating an interrupt vector table for use by embedded software, wherein a plurality of interrupts included in said interrupt vector table are generated by said selected configuration.
10. (original) The method according to Claim 1 further comprising:
- d) tracking said selected configuration; and
  - e) informing the user if said selected configuration is achievable using said hardware resources.
11. (original) A method for configuring a microcontroller containing a plurality of dynamically configurable blocks comprising:
- a) accessing a description of said dynamically configurable blocks, wherein said dynamically configurable blocks can be configured to produce a variety of functions;
  - b) selecting available configurations of said dynamically configurable blocks, wherein said selecting produces a selected configuration; and
  - c) generating configuration information corresponding to said selected configuration.
12. (original) The method according to Claim 11 wherein said description of said dynamically configurable blocks is substantially compliant with extensible markup language.
13. (original) The method according to Claim 11 further comprising:

a1) accessing predetermined configurations of said dynamically configurable blocks, wherein said predetermined configurations are user modules.

14. (original) The method according to Claim 13 further comprising:

b1) selecting said user modules to produce said selected configuration.

15. (original) The method according to Claim 11 further comprising:

c1) generating microprocessor instructions for using said configuration information to configure said dynamically configurable blocks.

16. (original) The method according to Claim 11 further comprising:

c2) generating application programming interface calls for embedded software.

17. (original) The method according to Claim 16 wherein said application programming interface calls are named according to names given to configurations of said hardware resources.

18. (original) The method according to Claim 11 further comprising:

c3) generating an interrupt vector table for use by embedded software, wherein a plurality of interrupts included in said interrupt vector table are generated by said selected configuration.

19. (original) The method according to Claim 11 further comprising:

d) editing said description to reflect changes in said plurality of dynamically configurable blocks.

20. (original) The method according to Claim 11 further comprising:

e) adding a file to a directory to enable the use of a new hardware configuration of said microcontroller.

21. (original) The method according to Claim 11 further comprising:  
f) adding a file to a directory to enable the user of a new user module.
22. (original) A system comprising:  
a processor coupled to a bus;  
a memory coupled to said bus and wherein said memory contains instructions that when executed on said processor implement a method for configuring a microcontroller, said method comprising:  
a) accessing a description of hardware resources of said microcontroller;  
b) selecting available configurations of said hardware resources of said microcontroller, wherein said selecting produces a selected configuration; and  
c) generating configuration information corresponding to said selected configuration.
23. (original) (original) A system as described in Claim 22 wherein said description of hardware resources comprises a text readable data structure.
24. (original) A system as described in Claim 23 wherein said text readable data structure is substantially compliant with extensible markup language.
25. (original) A system as described in Claim 22 wherein said method further comprises:  
a1) accessing predetermined configurations of said hardware resources, wherein said predetermined configurations are user modules.
26. (original) A system as described in Claim 22 wherein said method further comprises:  
b1) selecting said user modules to produce said selected configuration.

27. (original) A system as described in Claim 22 wherein said method further comprises:

c1) generating microprocessor instructions for using said configuration information to configure said microcontroller.

28. (original) A system as described in Claim 22 wherein said method further comprises:

c2) generating application programming interface calls for embedded software.

29. (original) A system as described in Claim 28 wherein said application programming interface calls are named according to names given to configurations of said hardware resources.

30. (original) A computer usable medium having computer readable code stored thereon for causing a computer system to perform a method for configuring a microcontroller, said method comprising:

a) accessing a description of hardware resources of said microcontroller;

b) selecting available configurations of said hardware resources of said microcontroller, wherein said selecting produces a selected configuration; and

c) generating configuration information corresponding to said selected configuration.

31. (original) The computer usable medium as described in Claim 30 wherein said description of hardware resources comprises a text readable data structure.

32. (original) The computer usable medium as described in Claim 31 wherein said text readable data structure is substantially compliant with extensible markup language.

33. (original) The computer usable medium as described in Claim 30 wherein said method further comprises:

a1) accessing predetermined configurations of said hardware resources, wherein said predetermined configurations are user modules.

34. (original) The computer usable medium as described in Claim 30 wherein said method further comprises:

b1) selecting said user modules to produce said selected configuration.

35. (original) The computer usable medium as described in Claim 30 wherein said method further comprises:

c1) generating microprocessor instructions for using said configuration information to configure said microcontroller.

36. (original) The computer usable medium as described in Claim 30 wherein said method further comprises:

c2) generating application programming interface calls for embedded software.

37. (original) The computer usable medium as described in Claim 36 wherein said application programming interface calls are named according to names given to configurations of said hardware resources.

Claims 38-41 (canceled) (election)

## RESPONSE TO RESTRICTION REQUIREMENT

In the Office Action mailed December 13, 2004, the Examiner has stated that the present Application contains two distinct inventions. As such, the Examiner has required the Applicants to elect a single invention for prosecution on the merits. Specifically, the Examiner has required the Applicants to elect between a first invention, Group I, recited in Claims 1-37, drawn to a method/a computer readable medium/system for configuring a microcontroller, classified in class 716, subclass 17, and a second invention, Group II, recited in Claims 38-41, drawn to a computer implemented graphical user interface, classified in class 345, subclass 661.

### ELECTION WITHOUT TRAVERSE BETWEEN GROUP I AND GROUP II

Applicants elect without traverse Group I, recited in Claims 1-37, drawn to a method/a computer readable medium/system for configuring a microcontroller, classified in class 716, subclass 17.

### Examiner Interview Summary

On December 8, 2004, Applicants' representative, Anthony Murabito, and Examiner Siek conducted a telephone interview. At that time a verbal election of Claims 1-37 for examination was made. That election is herein confirmed.

The Applicants thank Examiner Siek for the interview.